


Faculty Profile

Name of Faculty	Shaik Rahil Hussain	<p style="text-align: center;">Photo</p> 
Department	ECE	
Qualification	M.Tech, (Ph.D).,	
Designation	Assistant Professor	
Area of specialization	Wireless Communication and Networks, VLSI	
Date of Joining College	17 th Feb 2020	
Nature of Association (Regular/Contractual/Adjunct)	Regular	
e-mail	rahilvlsi@gmail.com	
No. of years of Experience	11 Years	

Academic Qualifications

- Pursuing Ph.D from VELTech University, Chennai.
- Completed M.Tech from K L University, Guntur
- Completed B.E. from Anna University, Chennai.

Working Experience Details

Sno	College / Organization	Dates
1	Mannan Institute of Science and Technology	06/08/2007-10/08/2010
2	Amrita Sai Institute of Science and Technology	27/04/2012-30/04/2014
3	DVR and Dr. HS MIC College of Technology	02/06/2014-26/04/2018
4	Srinivasa Institute of Engineering and Technology	01/06/2018-15/02/2020
5	Malineni Lakshmaiah Womens Engineering College	Feb 17, 2020 to till date

Subject taught

1. WCN, WSN, CN, CMC, DLCSO, DLD, CAO, VLSI, CMOS AICD

Professional Memberships:

1. ISRD
2. THE IRED
3. ISSE

HONORS / ACTIVITIES:

- Worked as Coordinator for NBA and NAAC at DVR & Dr. HS MIC College of Technology.
- Organized and Worked as Coordinator for conducting different workshops and Training Programs from BSNL, Vijayawada.
- Published a Technical paper on “**Virtual Circuit Based Smart NOC**” with ISSN: 2319-8753 in International Journal of Innovative Research in Science, Engineering and Technology Vol.5, Issue 11, Nov 2016, pp. 19330-19337.
- Published a Technical paper on “**A Delay Optimized Near Field Communication (NFC) Receiver Based on ECDSCA Algorithm**” with ISSN: 2319-8885 in International Journal Of Scientific Engineering and Technology Research Vol.3, Issue 47, Dec 2014, pp. 9673-9676.
- Published a Technical paper on “**Energy Efficient WSN using HEED Algorithm**” with ISSN: 2321-7545 in International Journal of Scientific Research and Education Vol.2, Issue 12, Dec 2014, pp. 2501-2507.
- Presented A paper titled “**A Nanopower Temperature Insensitive Programmable FGMOS Current Reference**” in IEEE international conference on ICGESCS’2013 at Arunai College of Engineering, Tiruvannamalai, Tamil Nadu.
- Published a Technical paper on “**Implementing High Secured and Fault Tolerant Baud Rate Synchronizer for NANO Applications**” with ISSN:2231-5381 in International Journal of Engineering Trends and Technology (IJETT) Vol.6, Issue 3, Nov-Dec 2013, pp. 594-599.
- Published a Technical paper on “**Low Power Coding Approach With Error Free Coding Scheme in VLSI Design**” with ISSN:2231-5381 in International Journal of Engineering Trends and Technology (IJETT) Vol.5, Issue 3, Nov-Dec 2013, pp. 59-65
- Published a Technical paper on “**VLSI Implementation of Densely Packed Decimal Converter to and from Binary Coded Decimal using Reversible Logic Gates**” with ISSN: 2248-9622 in International Journal of Engineering Research and Applications (IJERA) Vol. 2, Issue 3, May-Jun 2012, pp. 594-599.
- Published a Technical paper on “**High Speed and Low Power Dynamic Latched Comparator for Air Craft Application**” with ISSN:2248-9622 in International Journal of Engineering Research and Applications (IJERA) Vol. 2, Issue 3, May-Jun 2012, pp.1301-1312.
- Published a Technical paper on “**Low Power Synthesis in Digital Design by Automatic Insertion of Clock Gating and Operand Isolation Cells**” with ISSN:2248-9622 in International Journal of Engineering Research and Applications (IJERA) Vol. 2, Issue2, Mar-Apr 2012, pp.1086-1092.
- Published a Research paper in CLEAR International Journal of Research in Science and Technology (CLEAR IJRST) with title “**Effective and Implementation of Hypothesis Testing in Scientific Research**” with Online ISSN:2249-457X and Print ISSN: 2249 – 5991. Vol-01: issue 02-jul-dec 2011.
- Published a paper in CLEAR International Journal of Research in Applied Geosciences (CLEAR IJRAGS) on **Mapping of Geomorphologic Landforms of Miapur, Bollaram Areas of Ranga**

Reddy District, Hyderabad, Andhra Pradesh by Remote sensing and GIS techniques with Online ISSN:2277-1042 and Print ISSN: 2277 – 2162, Vol-01: No-01, Sep-Nov 2011.

- I was a coordinator for National Board of Accreditation and Diploma in Electronics and Communication in DVR & Dr. HS MIC College of Technology.
- I was a Timetable Incharge in the Department of ECE in DVR & Dr. HS MIC College of Technology.
- Participated in MietY, Govt. of India, Sponsored two week **Faculty Development Program on Signal Processing for Wireless Communication** organized by E & ICT Academy, National Institute of Technology, Warangal at DVR & Dr. HS MIC College of Technology.
- Participated in One Day National Convention on **Wireless Technology** at P.V.P.Siddhartha Engineering College.
- Participated in two week **Faculty Development Program on MACHINE LEARNING** at V.R.Siddhartha Engineering College.
- Participated in three day workshop on **Advanced Embedded and Real time operating systems** at DVR & Dr. HS MIC College of Technology.
- Participated in three day Seminar on **Recent trends and scope for research in signal processing and communications** at DVR & Dr. HS MIC College of Technology.
- Participated in three day workshop on **Digital VLSI Design using industry standard EDA tools and optimization methodologies** at DVR & Dr. HS MIC College of Technology.
- Was the in-charge organizer of our College and active participant in “INPLANT Training in Door Darshan Kendra” Chennai.
- Participated in two day national level workshop on SMART ANTENNAS at DVR & Dr. HS MIC College of Technology.
- Participated in two day national level workshop on VLSI Design at Vignan University, Guntur.
- Participated in one day national level workshop on LINUX at KL University, Vijayawada.
- Participated in the two-day State Level Paper Presentation on “Real Time Applications in Embedded Systems” at Anna University, Chennai.
- Participated in two day national level workshop on “Embedded Systems”, “Wireless Communication”, “FPGA”, “VLSI” at Anna University, Chennai.
- Participated in one day state level workshop on “PCB Design” at CAHCET, Vellore.
- Participated in 3 day National level workshop on Analog and Digital design in cadence and EDA tools at ACE Engineering College, Hyderabad.
- Participated in 2 day national level workshop on Xilinx and EDA tools at KL University, Vijayawada.