

1. Name : **Kundurthi Ravi Kumar**
 2. Contact Number : +919347041081/+919182019316
 3. E-MailID : kundurthi.ravikumar@gmail.com / Kundurthi_ravi@yahoo.com
 4. Date of Birth : 23.08.1971
 5. Highest Qualification : M.Tech
 6. Academic Performance (Descending Order)



S.No	Degree	University/ Institution	Year of Pass	% of Marks	Class
1	M Tech	JNTUA College of Engineering, Anantapur. JNTUA	2012	66	FIRST
2	B Tech	Koneru Lakshmaiah College of Engineering Kunchanapally, Guntur., N.U	1993	58	SECOND
3	Intermediate	Hindu College, Guntur.	1988	70	FIRST
4	SSC	Sri Majety Guravaiah High School, Guntur.	1986	74	FIRST

7. Total Years of Experience : 27 .

S. No.	Period		Organization / Institution	Position Held
	From	To		
1	19.11.2010	Till date	Malineni Lakshmaiah Women's Engineering College, Guntur.	Associate Professor
2	1.6.2009	18.11.2010	RK College of Engineering, Vijayawada	Assistant Professor
3	31.10.2008	31.5.2009	SVVSN Engineering College, Ongole.	Assistant Professor
4	12.7.2007	30.10.2008	PBR VITS, Kavali.	Assistant Professor
5	1.6.2003	11.7.2007	Malineni Lakshmaiah Engineering College, Singarayakonda.	Assistant Professor
6	30.5.1999	31.5.2003	College of Engineering and Technology, Secunderabad	Assistant Professor
7	5.10.1993	29.5.1999	Sannihitha EICS Limited, Hyderabad.	Production Engineer

8. Date of joining in present Institution : 19.11.2010
 9. Present Status : **Associate Professor**

10. Achievements since date of joining :

S. No.	Achievements	Year	Particulars
1.	JNTUK ratified Faculty	2014	JNTUK,KAKINADA
2.	Worked as HOD.	2015-16	MLWEC, Guntur.
3.	Worked as M.Tech Co-ordinator .	2012 -16	MLWEC, Guntur.
4.	Qualified JNTUK PhD entrance Examination, Short listed for final interview.	2015-16	JNTUK
5.	Participated in the Engineering Education & Research Seminar at Vijayawada on 29 th December 2015, conducted by National Instruments India	2015	Vijayawada.
6.	Attended in Faculty Improvement Program(FIP) on VLSI Design from 15 th to 19 th Nov 2016, conducted by APSSDC(Andhra Pradesh Skill Development Corporation) and LUCID VLSI.	2016	SRK College of Engineering , Vijayawada.
7.	IAENG Member, Member Number 227741.	2018	International Association Engineers
8.	Successfully completed online Faculty Development Program(FDP) on Advanced Python Programming using Django from 21 st - 25 th May 2020	2020	MLWEC, Guntur.
9.	Successfully completed APSSDC online Faculty Development Program(FDP) on Embedded Systems from 25 th May 2020 to 6 th June 2020	2020	Online APSSDC
10.	Participated in 5 days online Faculty Development Program(FDP) on "Innovative Trends in Data Analysis with AI" from 26 th to 30 th May 2020.Presented by Codegnan IT Solutions.	2020	Online Codegnan IT Solutions
11.	Participated in 2 days online Faculty Development Program(FDP) on MATLAB and Simulink in Engineering Education organised by APSSDC from 10-06-2020 to 11-06-2020	2020	Online APSSDC
12.	Successfully participated in a one day webinar on Self-Assessment & Self – Awareness, A Hands-on Approach " held on 1-07-2020 organized by department of ECM	2020	J.B. Institute of Engineering & Technology.
13.	Successfully Completed the Building a Smart Device with Amzon Alexa – IOT Webinar in Make Skilled Platform of Madblocks Technologies Pvt Limited on 6-7-2020	2020	Madblocks - Ignite
14.	Participated in 5 days online Faculty	2020	MLWEC, Guntur.

	Development Program(FDP) on "Image Processing and Computer Vision-Industrial Scope" from 8 th to 12 th July 2020.Organized by Department of ECE.		
15.	Participated in webinar on 12-07-2020 online Faculty Development Program(FDP) on Antennas organized by APSSDC.	2020	Online APSSDC
16.	Participated inLeadership talk with Dr.Pramod Chaudhari(16-5-2020),Mr.Mahesh Babu CEO Mahindra Electric Mobility Ltd.Dr.Nilesh N Oak (Indian Civilization & History) 30-05-2020,Ms.Ashwini Deshpande(Co-Founder & Director Elephant Design)06-06-2020,Shri R Subrahmanyam,IAS(Secretary Ministry of Social Justice&Empowerment,13-6-2020,Mr.Shridhar Venkat,CEO (Akshay Patra Foundation) 18-6-2020	2020	Online MHRD , MHRD's INNOVATION CELL (GOVERNMENT OF INDIA)
17.	Guided several M.Tech projects from 2012 to 2017.		
18.	Guided several B.Tech projects from 2003 to 2020.		

Papers Published

1. Manchester Encoding in SOLS for DSRC Applications in International Journal for Research in Technological Studies(IJRTS) ISSN:2348-1439,Volume 2,Issue 11,October, 2015
2. Design of Low Power Multiplier by using ANT & RPR Schemas in International Journal for Research in Technological Studies (IJRTS) ISSN:2348-1439,Volume 2,Issue 11,October, 2015
3. An Effective Scheming Of Power Efficient Circuits by Using Reversible Logic in International Journal of Reviews on Recent Electronics and Computer Science (IJRECS) ISSN:2321-5461,Volume 3,Issue 12,December, 2015
4. Design of Aging Aware Hybrid Logic Level Multiplier in International Journal of Research (IJR) ISSN:2348-6848,Volume 4,Issue 06,May, 2017
5. Design an High Security Cryptography Using Galio Field in International Journal of Advanced Scientific Technologies, Engineering and Management Sciences(IJASTEMS) ISSN:2454-356X,Volume 3,Issue 05,May, 2017
6. VLSI Design of a Novel Pre-Encoding Multiplier using Dadda Multiplier in International Journal of Professional Engineering Studies (IJPRES) ISS 4-29,Volume 8, Issue 4, May, 2017
7. Implementation of Floating Point FFT Using Parallel Prefix Multiplier for Fast Operation in Journal of Applied Science and Computations(JASC) ISSN:1076-5131,Volume VI, Issue IV, April, 2019

Carrier Objective

To obtain a long-term career with an organization, which has a strong background, that provides good opportunity for enhancement of professional and personal status.

Major Strengths	Major Weaknesses
1.Hard working. 2. Quality teaching. 3. Honesty in everything.	1. Too rigid towards perfection

SUBJECTS HANDLED:

**OC,SC, AWP,MWE,CS,EMWTL,S&S,EMF,DLD,EDC, STLD, ALS, LDSA, LDICA,
LICA, DSP, VLSI Systems Design ,Embedded Systems, Electronic Circuits, PTSP.**



Signature

Place :

Date:

(K.RAVI KUMAR)

